

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	
Rino Micheloni et al.	§	Art Unit: 2113
	§	
Serial No.: 10/805,182	§	Examiner: Joseph D. Manoskey
	§	
Filed: March 19, 2004	§	Conf. No.: 7411
	§	
For: Integrated Memory System	§	Docket: NUM.0098US
	§	03-AG-078

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

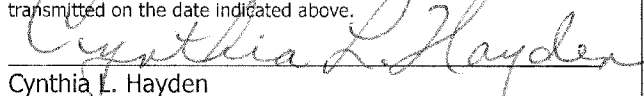
REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed December 1, 2009, please amend the above-referenced patent application as follows:

Date of Deposit: January 14, 2010

I hereby certify that this correspondence is being electronically transmitted on the date indicated above.


Cynthia L. Hayden